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PATENT
Attorney Docket No.: 10016663-1
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Eric DeLano
Serial No. 10/083,872
Filed: February 27, 2002
For: Configuration of Multi-Cluster
Processor from Single Wide
Thread to Two Half-Width
Threads

Confirmation No. 4721
Group Art Unit: 2183
Examiner: LI, AIMEE J

November 9, 2005

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Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE AND AMENDMENT

Sir:

Applicants filed a Response to the June 27, 2005, Office Action by Express Mail Label No. EV413240601US, deposited with the U.S. Postal Service on August 12, 2005. The U.S. Postal Service Website shows that the package including the Response was accepted on August 12, 2005 and delivered to the Patent Office on August 15, 2005. However, upon review of PAIR and telephonic discussions with Examiners Chan and Li on November 8, 2005, it is apparent that this Response was lost at the Patent Office. We therefore now resubmit this Response to the Final Office Action mailed June 27, 2005. Our amendments and remarks (identical to the filing of June 27, 2005) are as follows:

In the claims begin on page 2 of this paper, and
Remarks begin on page 5 of this paper.

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